## IT-5005(1)-CBGS B.E. V Semester

Examination, June 2020

## Choice Based Grading System (CBGS) Microprocessor and Interfacing

Time: Three Hours

Maximum Marks: 70

*Note:* i) Attempt any five questions.

- ii) All questions carry equal marks.
- 1. a) Explain the architecture of microprocessor with suitable diagram?
  - b) What are addressing modes? Describe various addressing modes in short.
- 2. a) Explain top down and bottom up design in detail?
  - b) Were an Assembly Language Program (ALP) to count the odd and even numbers in series of numbers?
- 3. a) Write an ALP to perform the subtraction of two 16 bits binary number?
  - b) What are microcontrollers? Explain the working of microcontrollers in detail?
- 4. a) Explain the architecture of Intel 8051 microprocessor?
  - b) What is Co-processor? Describe Math-coprocessor in detail?

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- 5. a) Describe Interface I/O Port Addressing in detail.
  - b) Write an ALP to convert BCD data to Binary Data?
- Explain the interrupts of 8085 microprocessor? 6. a)
  - Describe 16550 programmable communication interface A/D in detail?
- Explain direct memory access controller with suitable diagram.
  - b) What is Bus? Describe ISA Bus and EISA Bus in detail?
- 8. a) What are shift registers? Explain with suitable example.
- b) Explain the working of Disk drive and Tap drive in detail?

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